

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	4	(integrated adj circuit and scaler and timing adj controller and peripheral)	US-PGPUB; USPAT	OR	OFF	2005/08/19 13:41
L2	0	(integrated adj circuit and scaler and timing adj controller and peripheral).clm.	US-PGPUB; USPAT	OR	OFF	2005/08/19 13:41
L3	1	(integrated adj circuit and scaler and timing adj controller).clm.	US-PGPUB; USPAT	OR	OFF	2005/08/19 13:41
L4	18806	(frequency near3 low\$2) same clock	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/19 13:43
L5	491646	lcd or liquid adj crystal	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/19 13:43
L6	1435	4 and 5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/19 13:43
L7	1442761	scal\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/19 13:44
L8	599	6 and 7	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/19 13:44
L9	540098	analog	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/19 13:44

L10	383	8 and 9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2005/08/19 13:44
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